

FIG. 1 (PRIOR ART)

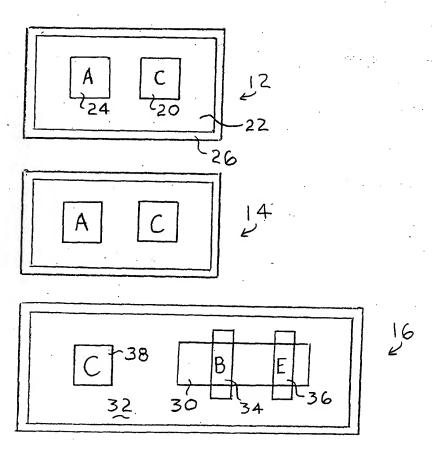
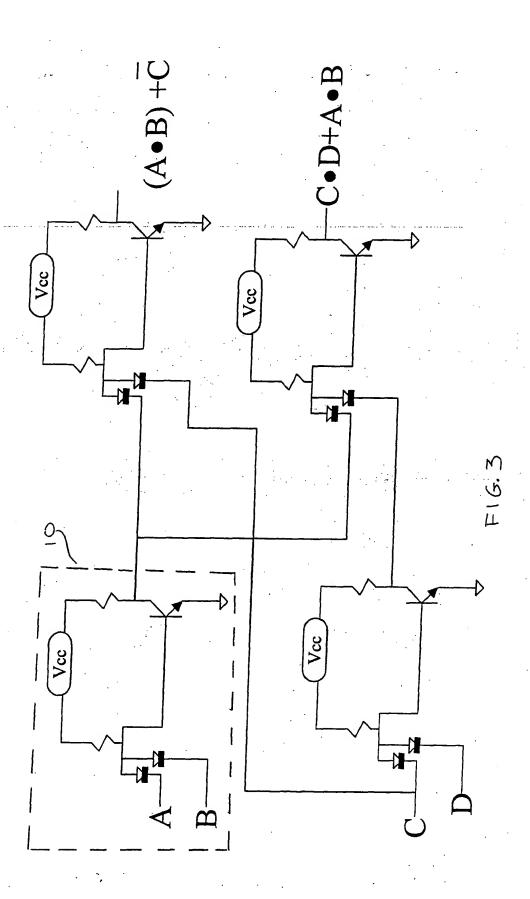
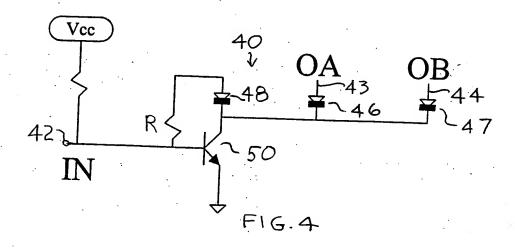
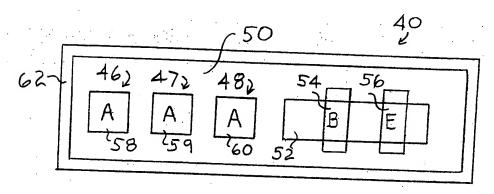


FIG.2 (PRIOR ART)







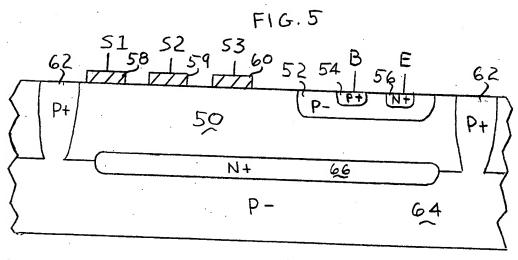
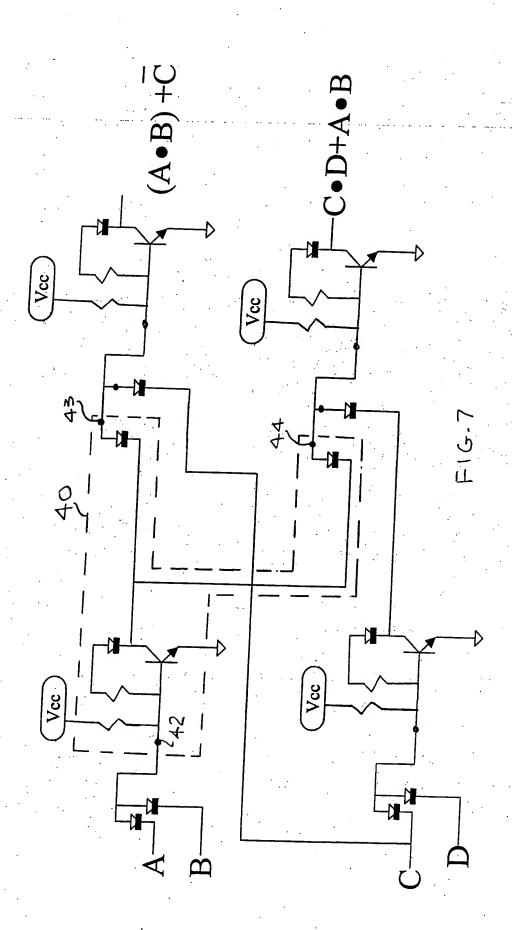


FIG. 6



Title: Integrated Schottky Transistor Logic Configuration Inventors: Thomas S. Wong et al. MIC-M082

